

RN2483 Module Errata and Data Sheet Clarification

The RN2483 module that you have received conforms functionally to the current Device Data Sheet (DS50002346B) and the “RN2483 LoRa® Technology Module Command Reference User’s Guide” (DS40001784E), except for the anomalies described in this document.

The module firmware issues are summarized in [Table 1](#).

The errata described in this document will be addressed in future revisions of the RN2483 module.

The module firmware version can be identified by issuing a System Get Version command (`sys get ver`).

For more information, refer to the “RN2483 LoRa® Technology Module Command Reference User’s Guide” (DS40001784).

Note: This document summarizes module firmware issues for the current, released to production firmware (1.0.1). Past module firmware issues are documented in *Appendix A. Current Firmware Features and Fixes* of the “RN2483 LoRa® Technology Module Command Reference User’s Guide” (DS40001784).

TABLE 1: MODULE FIRMWARE ISSUES SUMMARY

Item Number	Issue Summary	Affected Versions	
		0.9.5	1.0.1
1.	Sleep current out of specification	X	X
2.	GPIO pins default to ground	X	X

Module Errata Issues

Note: This document summarizes module firmware issues for the current, released to production firmware (1.0.1). Past module firmware issues are documented in *Appendix A. Current Firmware Features and Fixes* of the “RN2483 LoRa® Technology Module Command Reference User's Guide” (DS40001784).

Data Sheet Clarifications

There are no typographic corrections and clarifications to be noted for the latest version of the Device Data Sheet (DS50002346B).

1. Issue: Sleep current out of specification

Sleep mode current may draw 100-150 μ A.

Work around

There is no work around for this issue. This issue will be addressed in a future firmware revision.

Affected Firmware Revisions

0.9.5	1.0.1						
X	X						

2. Issue: GPIO pins default to ground

Upon initial power on of the module, the GPIO pins default to ground. Depending on the use of the GPIO pin, this may cause concern with connections to the GPIO pin that can cause a short to ground until the GPIO pin has been configured by the user.

Work around

Place a current limiting resistor in series with the GPIO connection. This issue will be addressed in a future firmware revision.

Affected Firmware Revisions

0.9.5	1.0.1						
X	X						

APPENDIX A: DOCUMENT REVISION HISTORY

Rev A Document (2/2016)

Initial release of this document.

RN2483

NOTES:

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